



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Ohtani et al.

Art Unit: 1765

Serial No.: 08/690,747

Examiner: R. Kunemund

1 / Find 1705

Filed

: August 1, 1996

Title

: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE

## Box AF

Assistant Commissioner for Patents

Washington, DC 20231

## TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Sir:

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims Independent

ANN #3203

Applicant hereby petitions under 37 C.F.R.

1.136 for a three month extension of time.

\$870

TOTAL FEE DUE:

\$1158\_

Applicant is filing a Notice of Appeal concurrently herewith.

A check for \$1158 is attached.

Respectfully submitted,

Jóhn D. Cowart Reg. No. 38,415

Fish & Richardson P.C.

4225 Executive Square, Suite 1400

La Jolla, California 92037

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Date of Deposit

I hereby certify that this correspondence is being deposited with the

United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Assistant Commissioner for Patents Washington, D.C. 20231.

Deborah Dean

102961.LJ1